

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1133	(712/10-22).CCLS.	USPAT; USOCR	OR	OFF	2006/01/26 09:56
S2	167	S1 and hard\$1wire\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/26 10:00
S3	43	S2 and (FPGA PLIC PLA PAL PLD "program\$1able logic")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 09:38
S4	175	S1 and (FPGA PLIC PLA PAL PLD "program\$1able logic")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/26 13:40
S5	94	S1 and (FPGA "program\$1able logic")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/26 10:29
S6	94	S1 and (FPGA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/26 13:37
S7	1133	(712/10-22).CCLS.	USPAT; USOCR	OR	OFF	2006/01/30 15:07
S8	167	S7 and hard\$1wire\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 09:38
S9	28	S8 and FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 15:14

S20	4	("communication bus" "communications bus") and ("pipeline bus") and router	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 12:54
S21	28	("communication bus" "communications bus") and ("pipeline bus")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 12:57
S34	278	hard\$1wire\$1 and router and pipeline and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 14:58
S35	30	S34 and S7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 15:01
S36	1	S35 and FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 15:02
S37	157	S7 and router	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 16:09
S38	0	S37 and (communication\$1 adj bus) and (pipeline adj bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 15:02
S39	5	S37 and (communication\$1 adj bus) and interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 15:03

S40	0	S39 and FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 15:03
S41	94	S7 and FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 15:13
S44	1	"1061439"	EPO	OR	OFF	2006/01/30 15:13
S45	26	S9 and flow	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 15:14
S46	17	"pipeline accelerator"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 16:05
S47	7	"pipeline accelerator" and router	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 16:05
S48	94	cascad\$3 adj pipeline\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/30 17:46
S49	60	(choose identify) adj pipeline	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 09:32
S50	160	(choose identify pick determine) adj pipeline	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 09:34
S51	1135	(712/10-22).CCLS.	USPAT; USOCR	OR	OFF	2006/01/31 09:32

S55	800	(choose identify pick determine) and S51	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 09:34
S56	2	((message instruction command) adj (choose identify pick determine signal) adj (pipeline\$1 "processing element" "processing elements" processor\$1 coprocessor\$1)) and S51	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 09:35
S57	11	((message instruction command) adj (choose identify pick determine signal) adj (pipeline\$1 "processing element" "processing elements"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 09:36
S58	4	((message instruction command) adj (choose identify pick determine signal) adj (pipeline\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 10:14
S63	1336	plurality adj pipeline\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 10:15
S66	2411	accelerator and pipeline	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/31 11:47
S67	55	S66 and S51	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/31 11:55
S68	157	S51 and router	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/31 11:56
S69	129	S68 and bus	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/31 11:56
S70	81	S69 and pipeline\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/31 13:08
S71	18	S69 and pipelines	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/31 11:56

S72	694	S51 and interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 14:15
S73	86	S72 and pipelines	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 15:02
S74	56	pipeline adj3 accelerator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 15:03
S78	88	pipeline with accept near2 data	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 17:00
S79	62	pipeline with accept adj data	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 17:02
S80	50926	pipelines	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 17:02
S81	6682	S80 and processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 17:02
S82	539	S81 and router	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 17:02

S83	450	S82 and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 17:02
S84	33	S83 and FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 17:03
S94	542	plurality adj pipelines	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/01 12:45
S96	2562	accelerator and pipeline	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/01 13:54
S97	246	S96 and router	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/01 13:54
S98	230	S97 and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/01 13:54
S100	148	reconfigurable adj interconnect	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/01 14:02
S109	255	data near1 pipeline near1 processing	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/01 16:54